

**Amendments to the Specification:**

Please replace the paragraph [0018], with the following rewritten paragraph:

Transmitter 12 also includes a driver circuit 24 for receiving the serialized differential input data signal from serializer 11~~serializer 20~~. Driver circuit 24 is an amplifier having a variable, frequency-selective gain and can be, for example, a finite impulse response driver that is arranged to be controlled, as will be explained below, to effect equalization, as needed, of the serialized differential input data signal. Receiver 14 also includes an amplifier 25 for receiving and amplifying the serialized differential input data signal conducted by data link 16. It should be noted that forming the differential input data signal from a single-ended input at input terminal 10 can be done by driver circuit 24 rather than by serializer 11 and that a single-ended output at output terminal 18 can be formed from the differential input data signal by amplifier 25 rather than by deserializer 11.